

FIG. 1

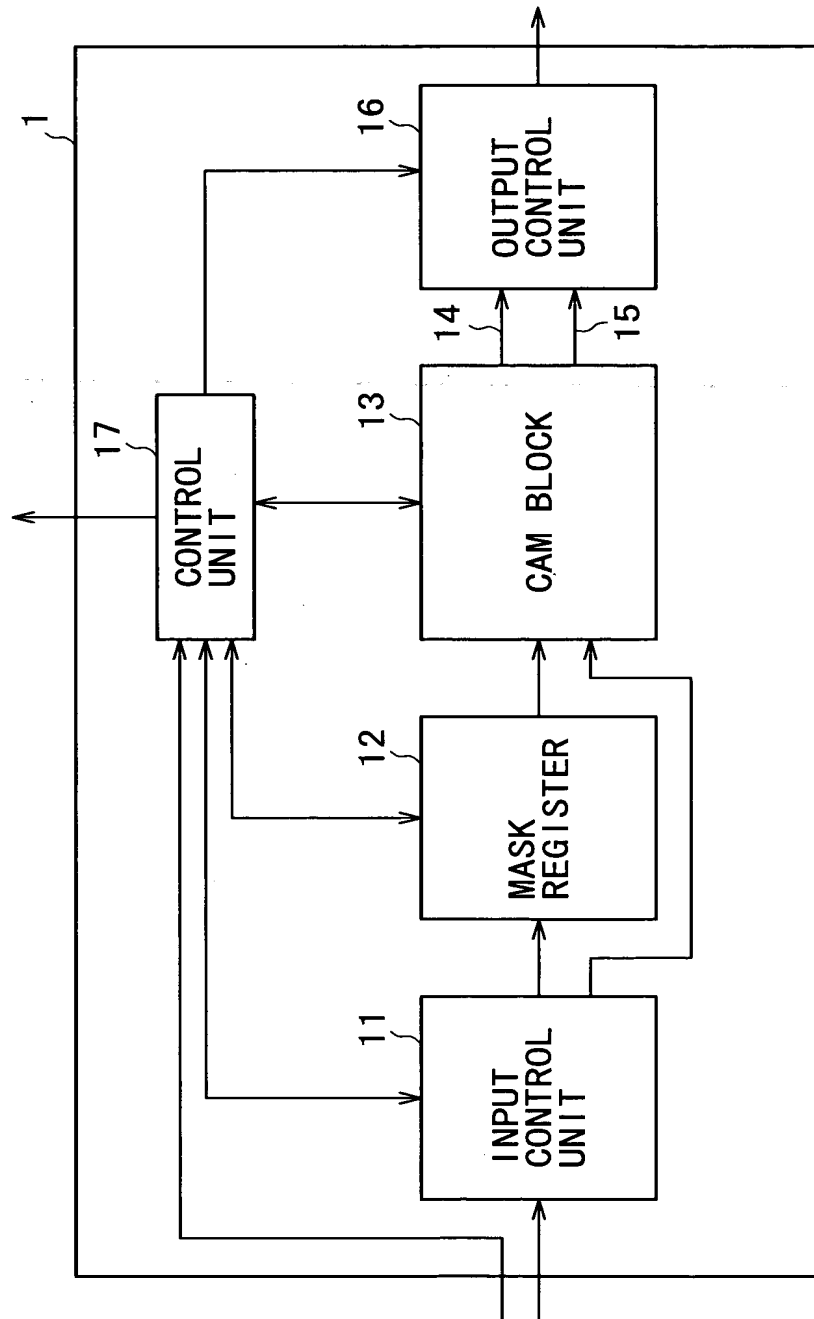


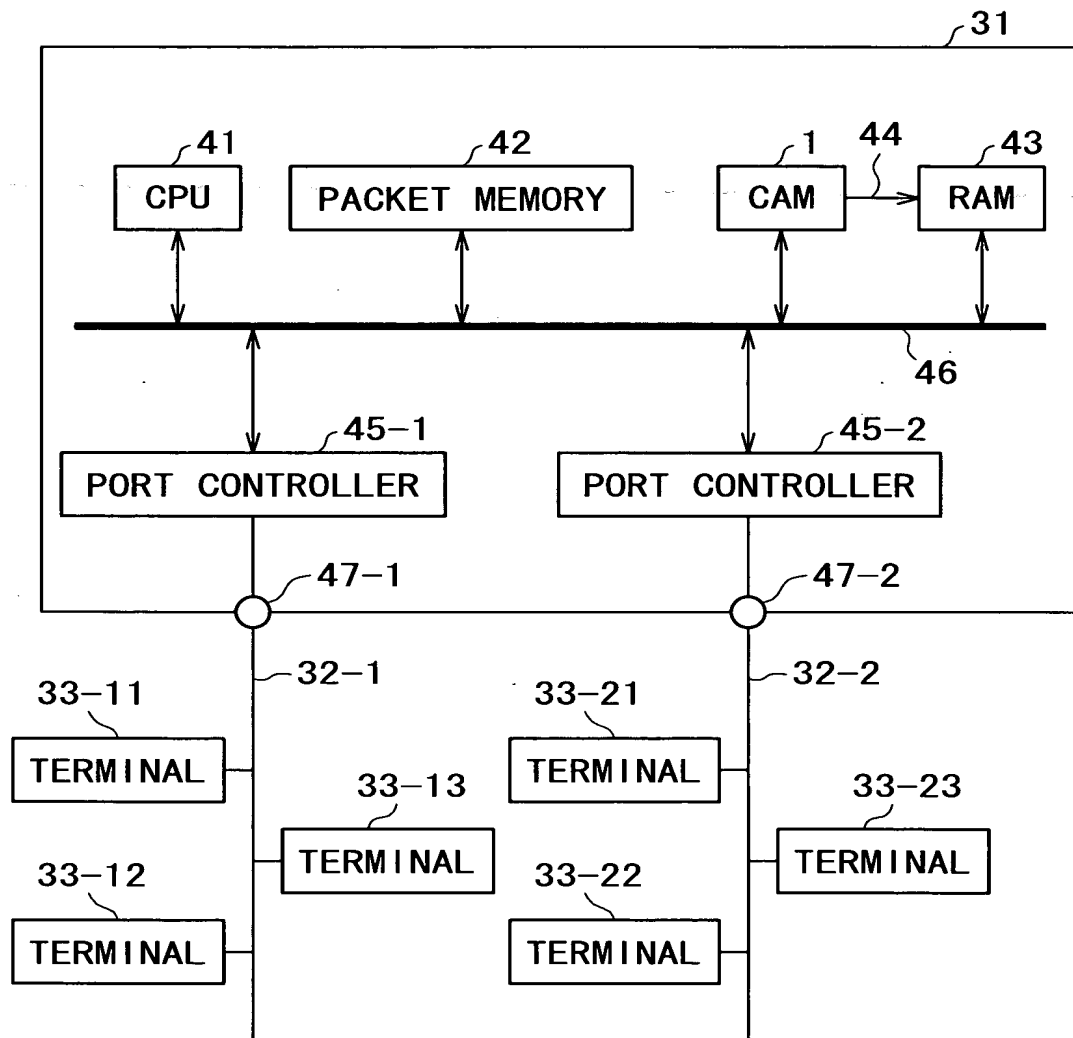
FIG. 2

m		
ADDRESS	STORED DATA	EMPTY BIT
xxx	xxx	1
xxx	xxx	0
⋮	⋮	

21-1

21-2

FIG. 3



F I G. 4

m			
ADDRESS	STORED DATA		EMPTY BIT
× × ×	NETWORK ADDRESS OF TERMINAL 33-11	TIME STAMP	1
× × ×	NETWORK ADDRESS OF TERMINAL 33-12	TIME STAMP	0
⋮	⋮		

21-1

21-2

F I G. 5

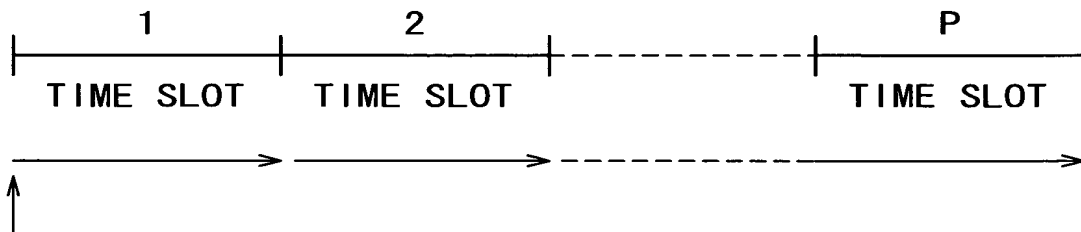


FIG. 6

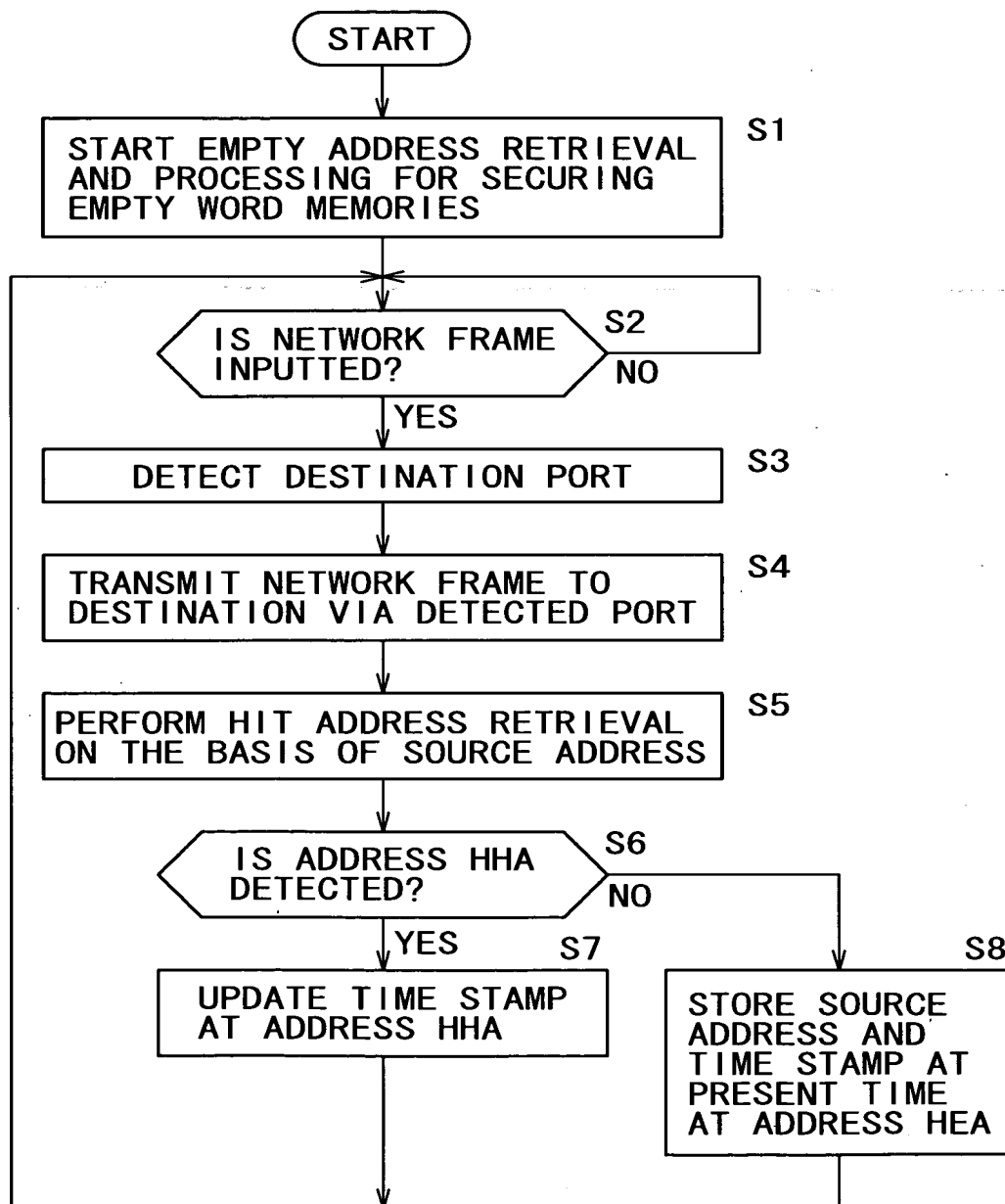


FIG. 7

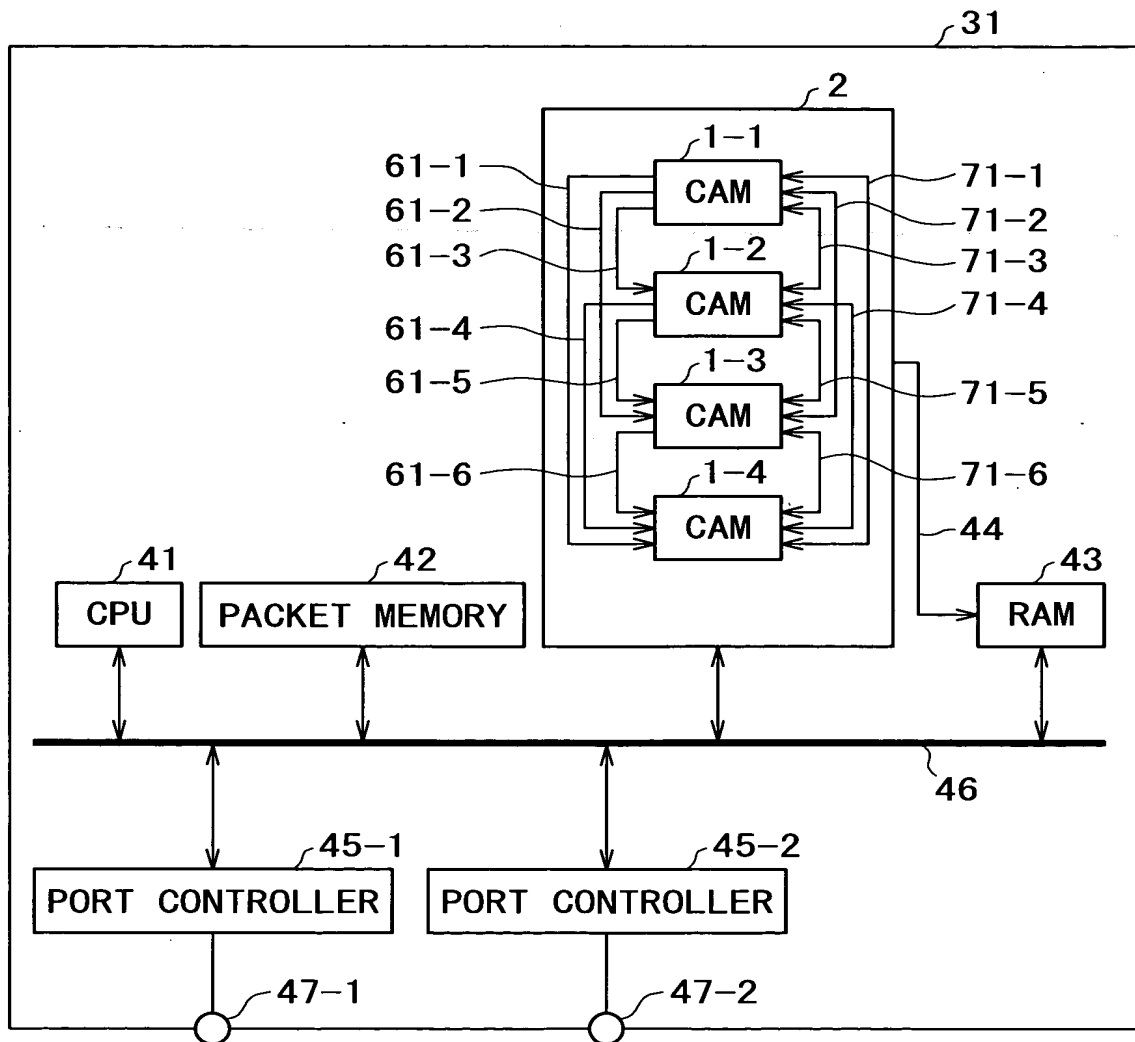


FIG. 8

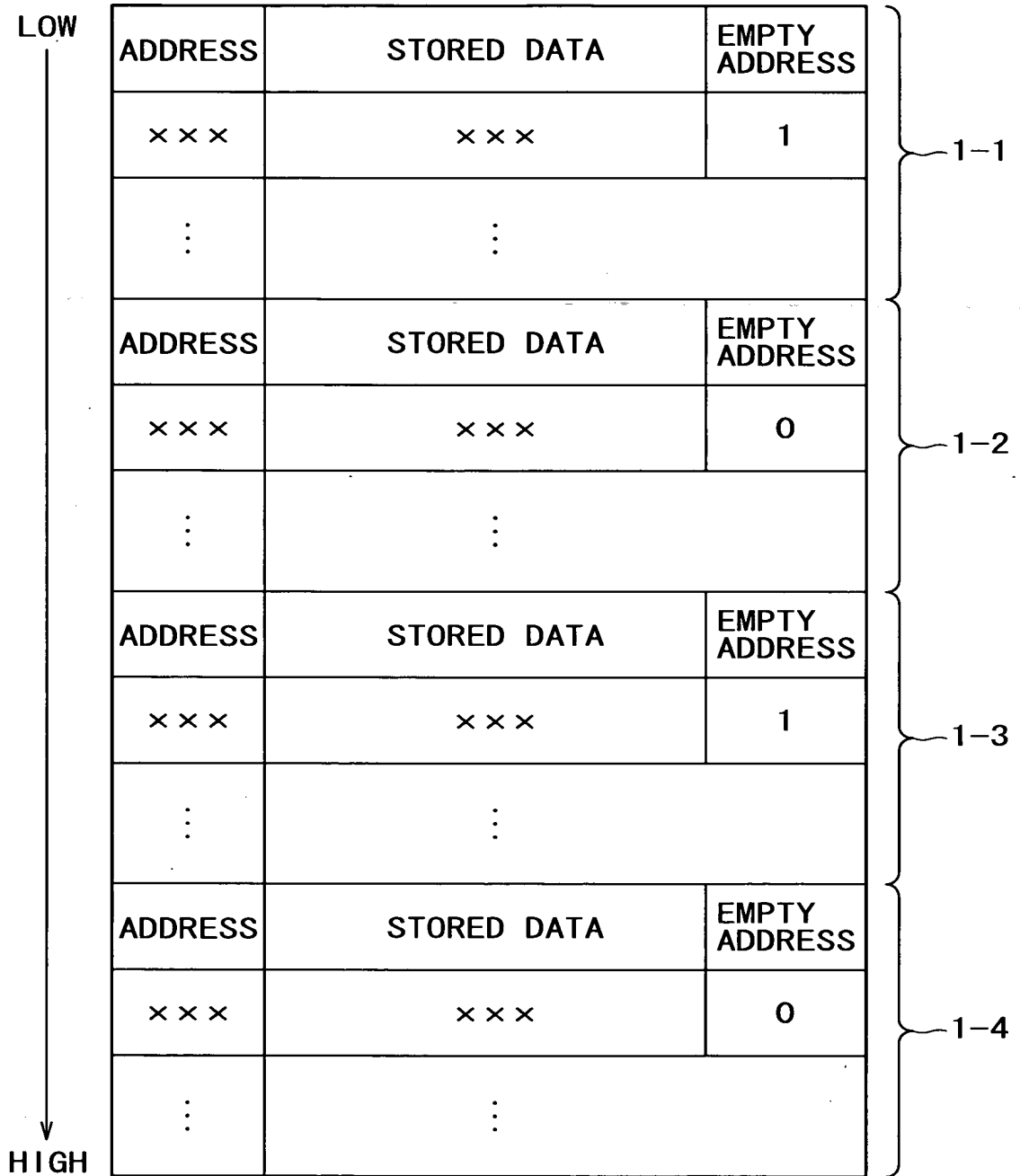


FIG. 9

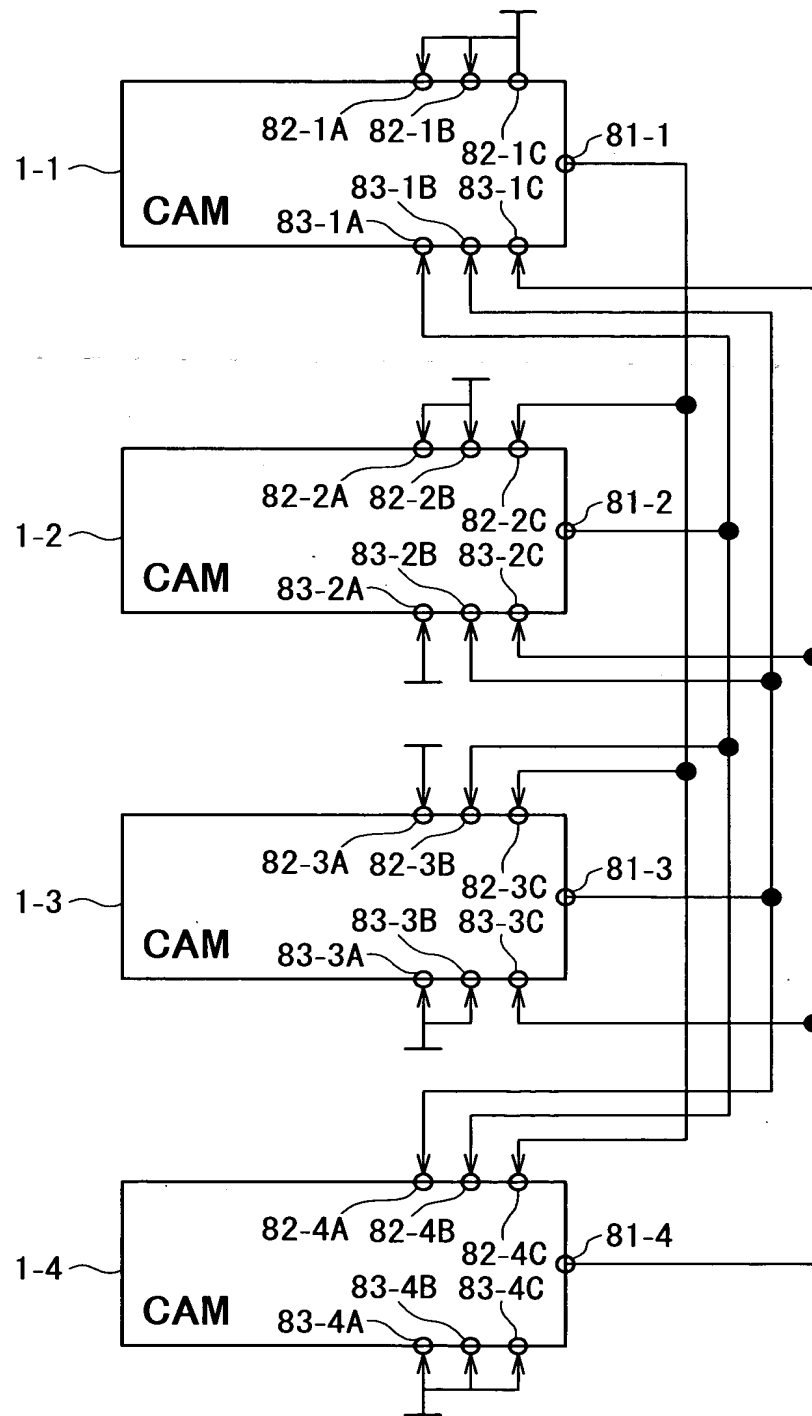


FIG. 10

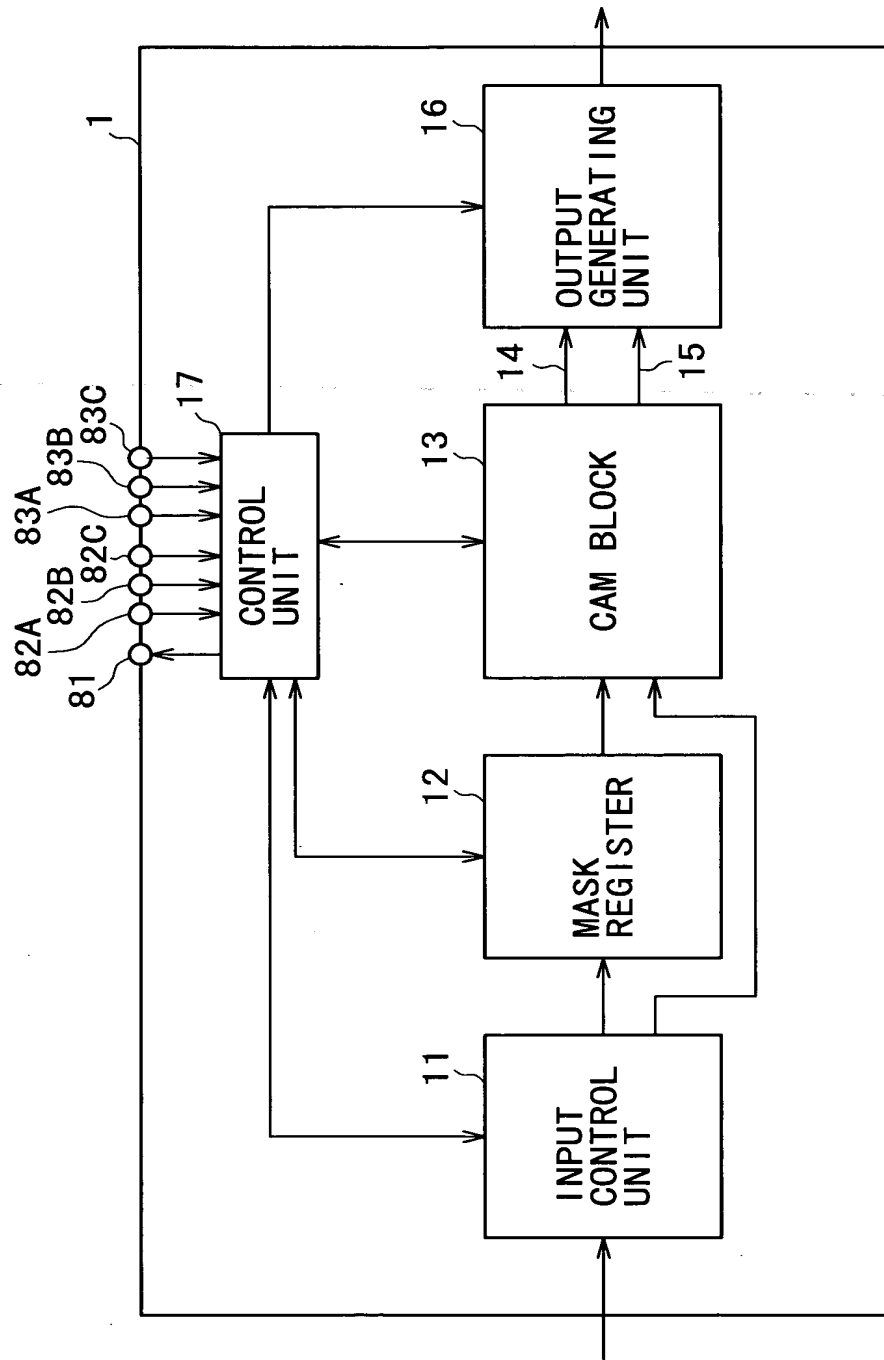




FIG. 12

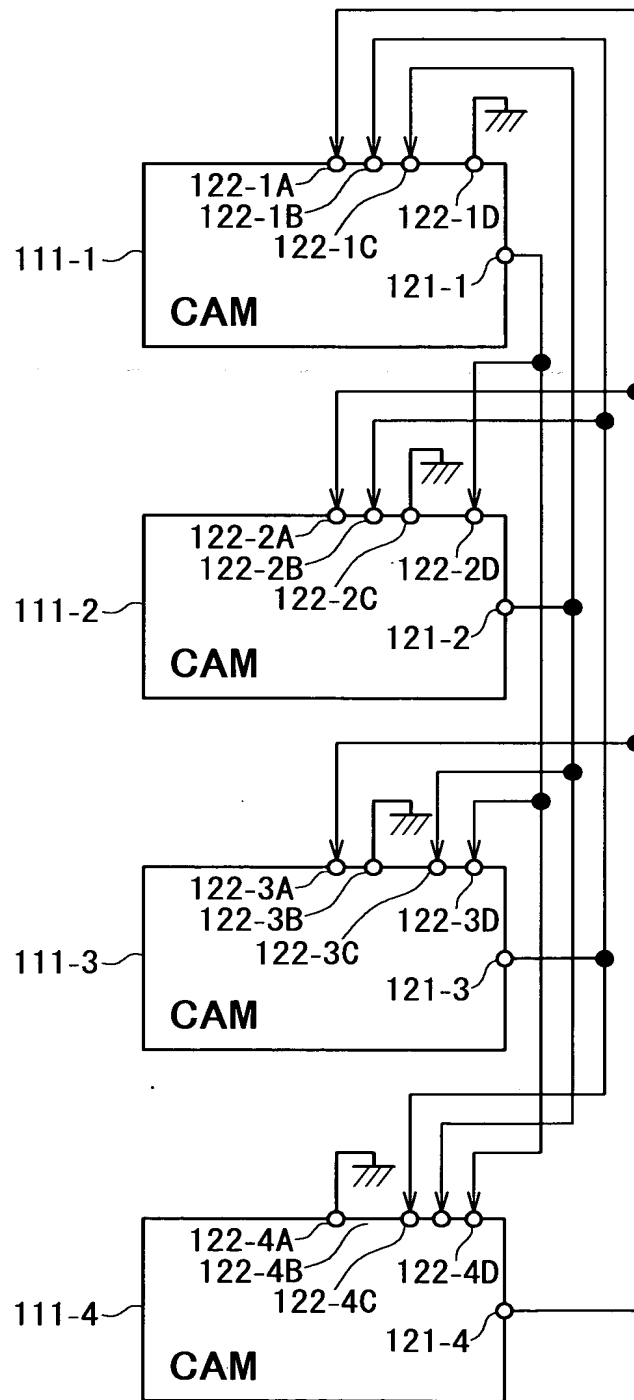


FIG. 13

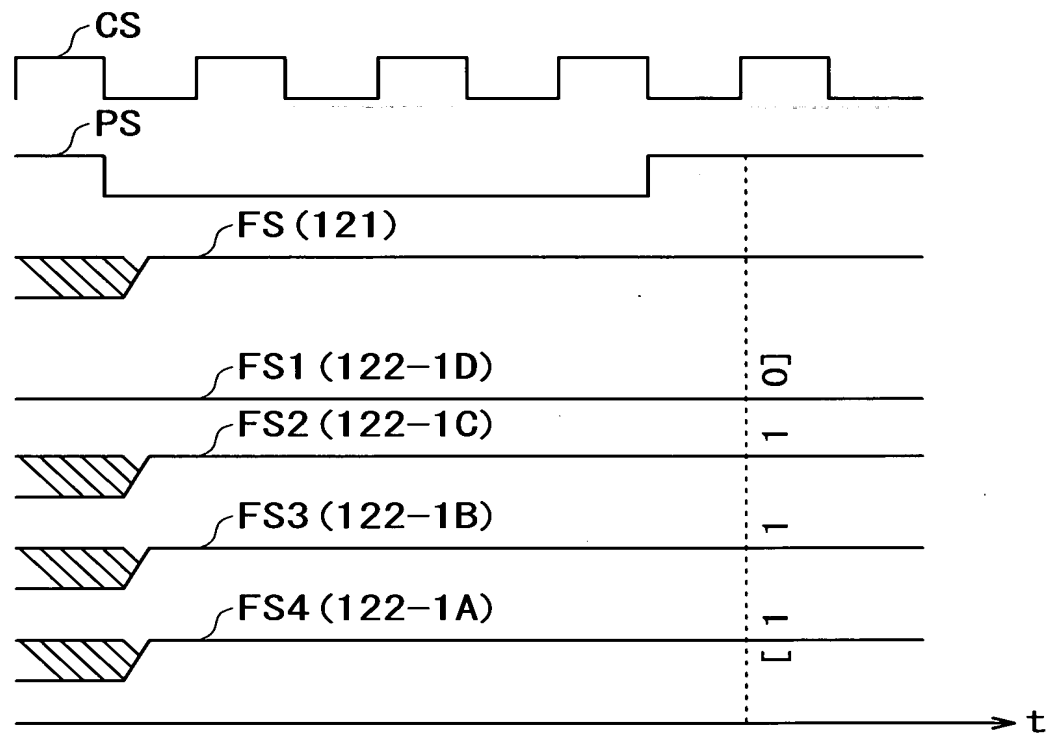


FIG. 14

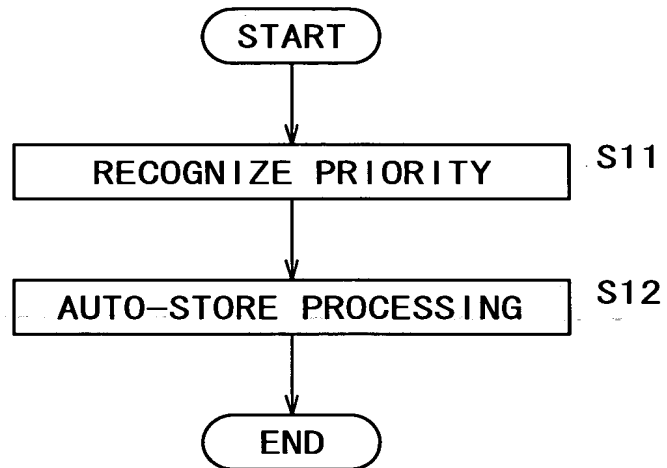


FIG. 15

INPUT SIGNAL [ABCD]	PROCESSING
[1110]	INTERNAL HIT
[1101]	INPUT SIGNAL[D] != 0&&, INTERNAL HIT
[1011]	INPUT SIGNAL[C] != 0&&, INPUT SIGNAL[D] != 0&& INTERNAL HIT
[0111]	INPUT SIGNAL[B] != 0&&, INPUT SIGNAL[C] != 0&& INPUT SIGNAL[D] != 0&&, INTERNAL HIT

FIG. 16

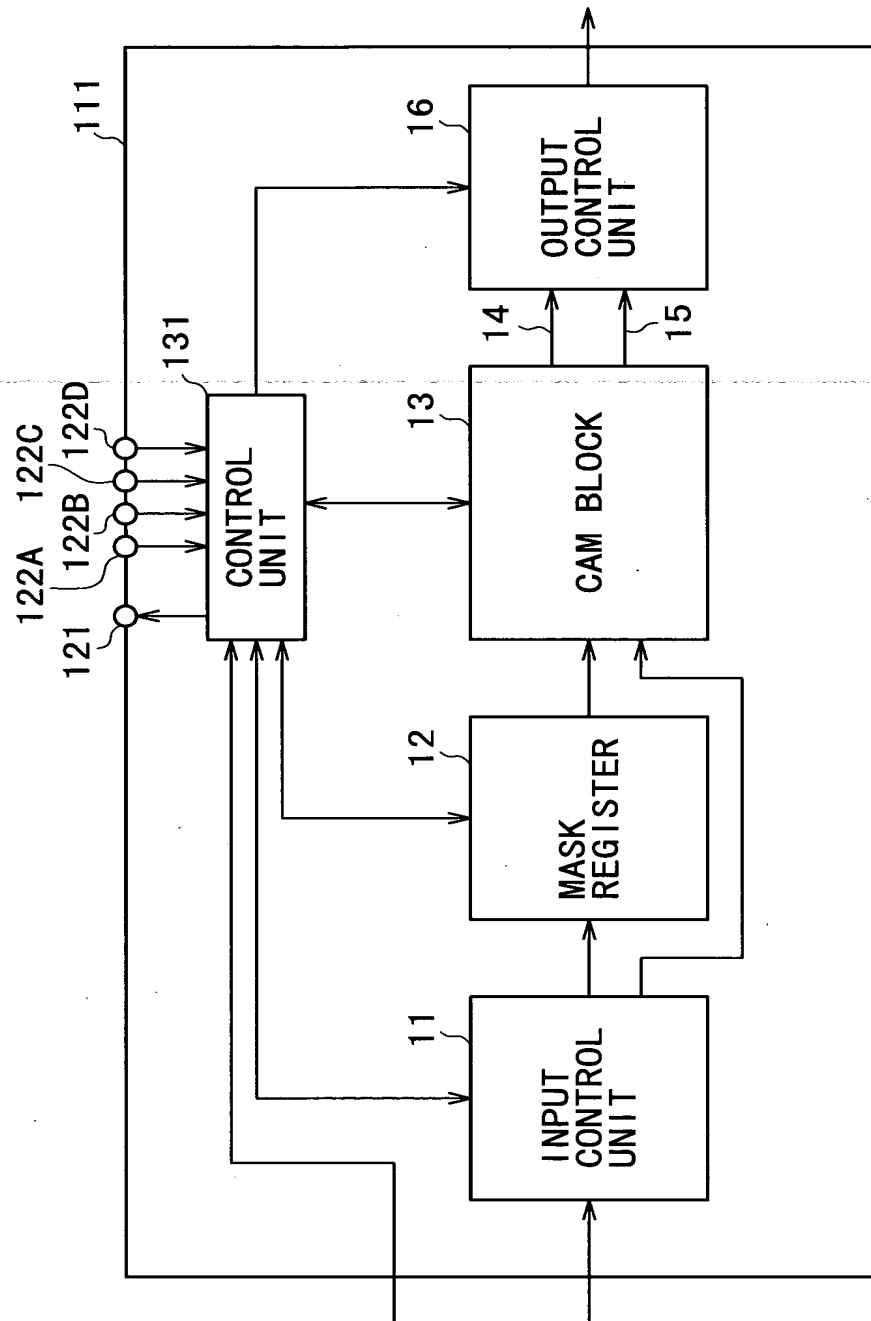
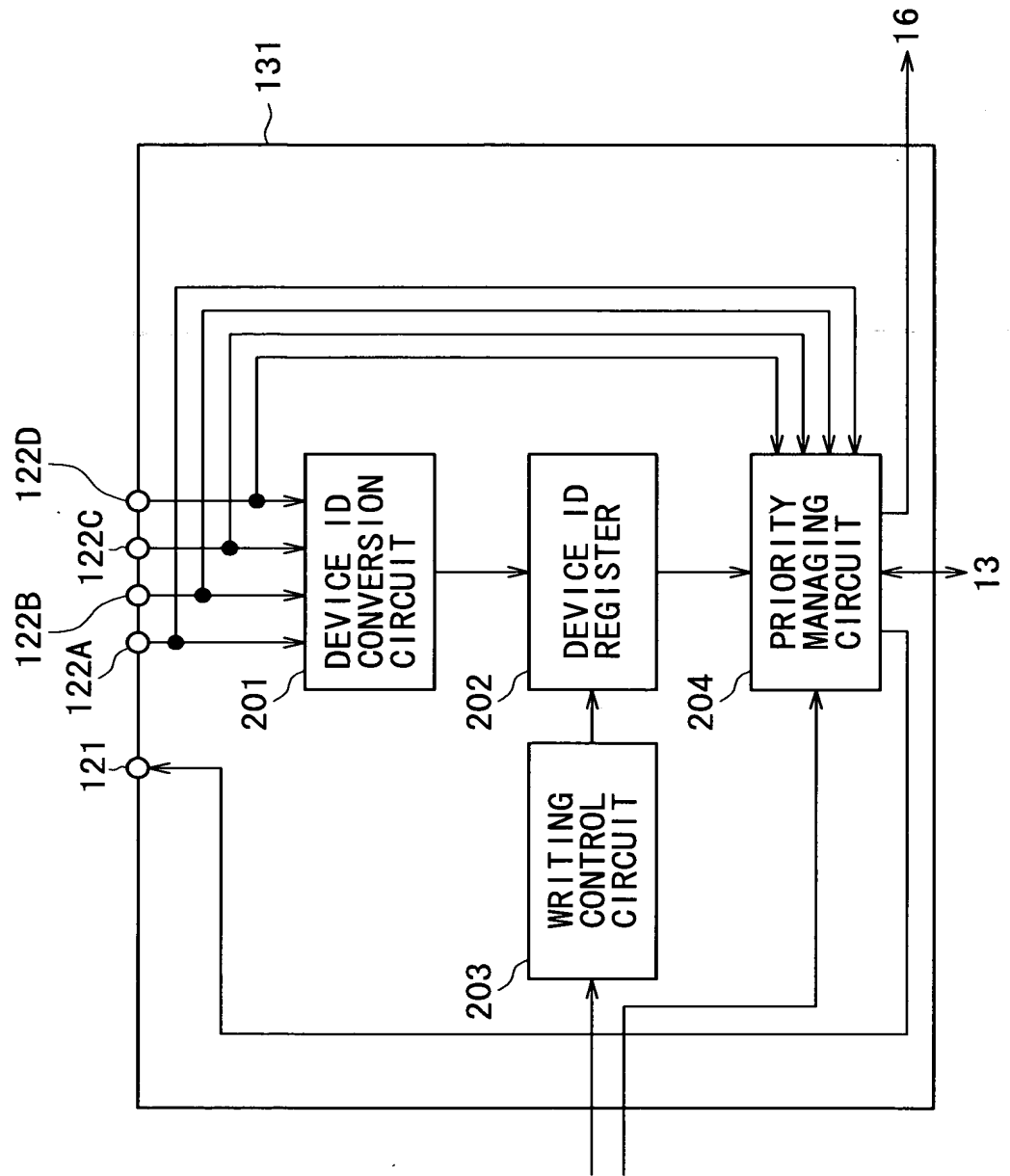


FIG. 17



F I G. 1 8

INPUT SIGNAL [ABCD]	DEVICE ID
[1110]	0
[1101]	1
[1011]	2
[0111]	3